INDIAN MARITIME UNIVERSITY

(A Central University, Govt. of India) End Semester Examinations- Dec 2019/Jan 2020 B.Tech (Marine Engineering) Semester-III

UG11T1302/2302- Electronics

Maximum Marks: 70

Date: 12.12.2019

Time: 3 Hrs. Pass Marks: 35 Part – A (compulsory) Answer the following (10x2=20 Marks) 1 In CE amplifiers, the purpose of input coupling capacitor is to-[2] a. Prevent battery current (DC) from flowing into input side b. To forward bias the base-emitter c. To restrict DC current from flowing into amplifier circuit d. To restrict AC current from flowing into amplifier circuit 2 If A is the amplifier gain and β is the feedback gain then, which of [2] the following represent Barkhausen's Criteria. b. $A\beta > 1$ and $\angle A\beta = 2\pi n$ a. $A\beta = 1$ and $\angle A\beta = 2\pi n$ c. $A\beta = 1$ and $\angle A\beta = \pi n$ d. A β >1 and $\angle A\beta = \pi n$ Where, n = 0, 1, 2, 3...3 Which class of amplifiers has minimum efficiency-[2] b. Class B d. None of these a. Class A c. Class AB 4 Which of the following circuits is called a comparator with [2] hysteresis? a. Oscillator b. Amplifier c. Sweep Generator d. Schmitt Trigger Briefly describe the concept of virtual ground with respect to a 5 [2] OPAMP based inverting amplifier. Which of the following digital device may be used to implement [2] 6 any two input, single output function. a. Two input AND gate b. 4:1 Multiplexer c. Half adder d. None of these 7 Which of the following is a specification of digital to analog [2] convertor? a. Turn ON time b. End of Conversion c. Start of Conversion d. Conversion Time A CMOS circuit is used to implement-[2] 8 a. An amplifier circuit b. An oscillator circuit c. A logic gate d. All of these

- **9** Which of the following devices may be used in the firing circuit of [2] SCRs.
 - a. Transistor b. UJT c. FET d. IGBT
- **10** Which of the AM transmission modes consumes maximum power? [2]a. DSB-TCb. SSB-TCc. SSB-SCd. DSC-SC

<u>Part – B</u> Answer any 5 out of 7 questions (5 x 10= 50 marks)

- **11 (a)** Draw a CE amplifier circuit and explain the purpose of various [5] components.
 - (b) With respect to a transistor, derive the expression for a in [5] terms of β and a in terms of γ .
- **12 (a)** Draw a well labelled diagram of Wein-Bridge oscillator and [5] explain its working. What is the formula for its output frequency?
 - (b) An RC Phase shift oscillator, using a BJT and RC network, is to [5] be designed to operate at 1 KHz. If the three resistors in RC phase shift network are $R_1=R_1=R_3=R=10$ K Ω , and all capacitors to have same values i.e. $C_1=C_2=C_3=C$, find the value of C.
- **13 (a)** Draw a well labelled diagram of a class AB complementary [5] symmetry push pull amplifier and explain its working.
 - (b) Explain the concept of cross-over distortion in class B power amplifier. Suggest a method to mitigate the effect of cross- [5] over distortion.
- 14 (a) Draw well labelled circuit diagram clamper circuit. [5]
 Draw the well labelled circuit diagram and explain working of a 555 timer IC based monostable multivibrator. [5]
- **15 (a)** Draw the circuit diagram of an OPAMP based differentiator [5] circuit and derive the expression of its output in terms of input and circuit components.
 - (b) Develop an op-amp based circuit so as to produce output [5] voltage Vo = $2V_1 5V_2 + 10V_3$, where V_1 , V_2 and V_3 are external input voltages.

- **16 (a)** Explain operation of D flip-flop. Show the truth table and [5] timing diagram.
 - (b) Write an assembly language program to add two 8 bit [5] numbers.
- **17 (a)** An AM wave equation is given as,[5]

 $v=5(1+0.6 \sin 150t) \sin(314 \times 10^4 t)$ Volts.

What are the minimum and maximum amplitudes of AM Wave? What are the amplitudes and frequencies of message signal, RF carrier and side bands?

(b) Draw block diagram and explain the purpose of each block of ^[5] a super-heterodyne receiver.